

U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.		Serial No.		
				M-8066 US		Unknown		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s)				
(Use several sheets if necessary)				Verdun, Gary J.				
				Filing Date		Group		
				Herewith		Unknown		
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
MC	AA	5,524,248	10 Jul. 1993	Parks et al.				
MC	AB	5,410,711	4 Feb. 1995	Stewart				
MC	AC	5,404,546	4 Feb. 1995	Stewart				
MC	AD	5,404,907	4 Mar. 1996	Sato et al.				
MC	AE	5,603,040	2 Jan. 1997	Bell et al. Frager				
MC	AF	5,675,814	10 Jan. 1997	Pearce				
MC	AG	5,944,831	8 Jan. 1999	Pate et al.				
	AH							
	AI							
	AJ							
	AK							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AQ	Amy Nelson and John Stuewe, "Method And Apparatus For Determining Continuity And Integrity Of A Rambus Channel In A Computer System"; January 26, 1999; Serial No.: 09/237,704. (Copy not enclosed..)						
	AR	Steven Lash, Stephanus Saputro, Doug Wallace, Jr., "Data Processing Systems Having Mismatched Impedance Components"; December 23, 1999. (Copy not enclosed.)						
	AS	Jim Walker and Charles Zeller, "Power Management Override For Portable Computers"; August 4, 1995; Serial No.: 08/511,871. (Copy not enclosed.)						
Examiner		Date Considered 10/2/02						
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								

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	AL							
	AM							
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AQ	Alan Christensen and Fritz Koehler, "Method And Apparatus For Preventing Inadvertent Power Management Time-Outs"; Janaury 17, 1997; Serial No. 08/785,373. (Copy not enclosed.)						
mc	AR	Intel 820 Chipset: A Revolutionary Architecture For Mainstream Performance PCs in 2000, ftp://download.intel.com/design/chipsets/820/820white.pdf .						
	AS							
Examiner		Date Considered						
		10/02/02						
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